ELECTRONICS, INC.
44 FARRAND STREET
BLOOMFIELD, NJ 07003
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NTE1816
Integrated Circuit
Module, Dual AF PO, 6W/Ch
Dual Power Supplies Req'd

Features:
- Built–In Muting Circuit to Cut Off Various Kinds of Shock Noise.
- Greatly Reduced Heat Sink due to Case Temperature +125°C Guaranteed.

Absolute Maximum Ratings: (T_A = +25°C unless otherwise specified)

Maximum Supply Voltage, V_{CC}max . . . . . ±20.5V
Junction Temperature, T_J . . . . . +150°C
Operating Case Temperature, T_C . . . . . +125°C
Storage Temperature, T_{stg} . . . . . –30° to +125°C
Thermal Resistance, Junction–to–Case, R_{thJC} . . . . . 5°C/W
Available Time for Load Shorted (V_{CC} = ±13.2V, R_L = 8Ω, f = 50Hz, P_O 6W), t_s 2sec

Recommended Operating Conditions: (T_A = +25°C unless otherwise specified)

Recommended Supply Voltage, V_{CC} 13.2V
Recommended Load Resistance, R_L 8Ω

Electrical Characteristics: (T_A = +25°C, V_{CC} = ±13.2V, R_L = 8Ω, R_g = 600Ω, VG = 40dB unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}		20	40	100	mA
Output Power	$P_{O(1)}$	THD = 0.4%, f = 20Hz to 20kHz	6	–	–	W
		V_{CC} = ±12V, THD = 1%, R_L = 4Ω, f = 1kHz	6	–	–	W
Total Harmonic Distortion	THD	P_O = 1W, f = 1kHz	–	–	0.3	W
Frequency Characteristics	fL, fH	P_O = 1W, –3dB	20 to 50k			Hz
Input Resistance	r_i	P_O = 1W, f = 1kHz	–	55	–	kΩ
Output Noise Voltage	V_{NO}	V_{CC} = ±17V, Rg = 10kΩ	–	–	1.2	mV_{rms}
Middle Point Voltage	V_N	V_{CC} = ±17V	–70	0	70	mV
Muting Voltage	V_M		–2	–5	–10	V

Pin Connection Diagram
(Front View)